



BC857BS

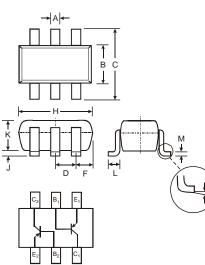
DUAL PNP SURFACE MOUNT SMALL SIGNAL TRANSIS

Features

- Ideally Suited for Automated Insertion
- For Switching and AF Amplifier Applications
- Ultra-Small Surface Mount Package
- Lead Free/RoHS Compliant (Note 2)
- "Green" Device (Note 4 and 5)

Mechanical Data

- Case: SOT-363
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Solderable per MIL-STD-202, Method 208
- Lead Free Plating (Matte Tin Finish annealed over Alloy 42 leadframe).
- Terminal Connections: See Diagram
- Marking: K3W (See Page 3)
- Ordering Information (See Page 3)
- Weight: 0.006 grams



SOT-363								
Min	Max							
0.10	0.30							
1.15	1.35							
2.00	2.20							
0.65 Nominal								
0.30	0.40							
1.80	2.20							
_	0.10							
0.90	1.00							
0.25	0.40							
0.10	0.25							
0°	8°							
All Dimensions in mm								
	Min 0.10 1.15 2.00 0.65 N 0.30 1.80 0.90 0.25 0.10 0°							

TOP VIEW

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic		Symbol	Value	Unit	
Collector-Base Voltage		V _{CBO}	-50	V	
Collector-Emitter Voltage		V _{CEO}	-45	V	
Emitter-Base Voltage		V _{EBO}	-5.0	V	
Collector Current	(Note 1)	Ιc	-100	mA	
Peak Collector Current	(Note 1)	I _{CM}	-200	mA	
Peak Base Current	(Note 1)	I _{BM}	-200	mA	
Power Dissipation at T _{SB} = 50°C	(Note 1)	Pd	200	mW	
Operating and Storage Temperature Range		T _j , T _{STG}	-55 to +125	°C	

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
DC Current Gain	(Note 3)	h _{FE}	220	—	475	_	$V_{CE} = -5.0V, I_{C} = -2.0mA$	
Thermal Resistance, Junction to Ambient Air	(Note 1)	$R_{\theta JA}$	—	—	625	°C/W	Note 1	
Collector-Emitter Saturation Voltage	(Note 3)	V _{CE(SAT)}	_	_	-100 -400	mV	I _C = -10mA, I _B = -0.5mA I _C = -100mA, I _B = -5.0mA	
Base-Emitter Saturation Voltage	(Note 3)	V _{BE(SAT)}	_	-700	—	mV	$I_{C} = -10mA$, $I_{B} = -0.5mA$	
Base-Emitter Voltage	(Note 3)	V _{BE}	-580	-665	-750	mV	$V_{CE} = -5.0V, I_{C} = -2.0mA$	
Collector Cutoff Current		I _{СВО} I _{СВО}	_		-15 -4.0	nA μA	V _{CB} = -30V, I _E = 0 V _{CB} = -30V, T _i = 150°C	
Emitter Cutoff Current		I _{EBO}	_	—	-100	nA	$V_{EB} = -5.0V, I_{C} = 0$	
Gain Bandwidth Product		f⊤	100	_	_	MHz	$V_{CE} = -5.0V, I_{C} = -10mA,$ f = 100MHz	
Collector-Base Capacitance		Ссво	_		3	pF	V _{CB} = -10V, f = 1.0MHz	
Emitter-Base Capacitance		CEBO	_	11		pF	V _{EB} = -0.5V, f = 1.0MHz	

Notes: 1. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

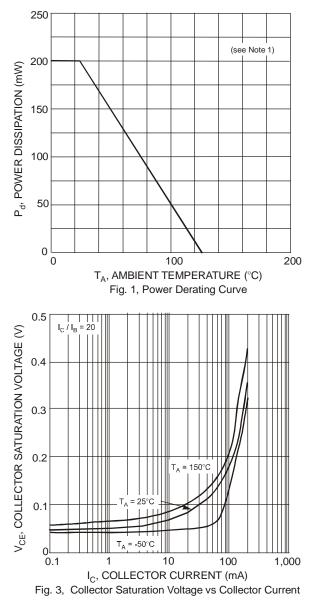
No purposefully added lead. 2.

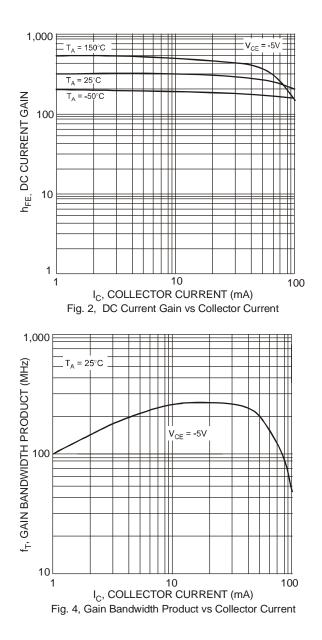
3. Short duration pulse test used to minimize self-heating effect.

4.

Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php. Product manufactured with Date Code UO (week 40, 2007) and newer are built with Green Molding Compound. Product manufactured prior to Date 5. Code UO are built with Non-Green Molding Compound and may contain Halogens or Sb2O3 Fire Retardants.







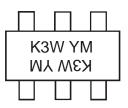


Ordering Information (Note 6)

Device	Packaging	Shipping
BC857BS-7-F	SOT-363	3000/Tape & Reel

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



 $\begin{array}{l} \mathsf{K3W} = \mathsf{Product Type Marking Code} \\ \mathsf{YM} = \mathsf{Date Code Marking} \\ \mathsf{Y} = \mathsf{Year ex: N} = 2002 \\ \mathsf{M} = \mathsf{Month ex: 9} = \mathsf{September} \end{array}$

Year	2002	2003	2004	2005	j 20	06 2	007	2008	20	09	2010	2011	2012
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